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Inventor Information for 10/090797

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
08427117	5596524	150	04/21/1995	CMOS MEMORY CELL WITH GATE OXIDE OF BOTH NMOS AND PMOS TRANSISTORS AS TUNNELING WINDOW FOR PROGRAM AND ERASE	LIN, JONATHAN
08447991	5594687	150	05/23/1995	COMPLETELY COMPLEMENTARY MOS MEMORY CELL WITH TUNNELING THROUGH THE NMOS AND PMOS TRANSISTORS DURING PROGRAM AND ERASE	LIN, JONATHAN
08466438	5754471	150	06/06/1995	LOW POWER CMOS ARRAY CELL FOR A PLD WITH PROGRAM AND ERASE USING CONTROLLED AVALANCHE INJECTION	LIN, JONATHAN
08497992	5742542	150	07/03/1995	NON-VOLATILE MEMORY CELLS USING ONLY POSITIVE CHARGE TO STORE DATA	LIN, JONATHAN
08500295	5700698	150	07/10/1995	METHOD FOR SCREENING NON-VOLATILE MEMORY AND PROGRAMMABLE LOGIC DEVICES	LIN, JONATHAN
08501230	5604370	150	07/11/1995	FIELD IMPLANT FOR SEMICONDUCTOR DEVICE	LIN, JONATHAN
08514300	Not Issued	161	08/11/1995	INTER-LEVEL DIELECTRIC (ILD) FOR A SEMICONDUCTOR DEVICE HAVING SILICIDE	LIN, JONATHAN
08551974	5615150	150	11/02/1995	CONTROL GATE-ADDRESSED CMOS NON-VOLATILE CELL THAT PROGRAMS THROUGH GATES OF CMOS	LIN, JONATHAN

				TRANSISTORS	
08554092	5587945	150	11/06/1995	CMOS EEPROM CELL WITH TUNNELING WINDOW IN THE READ PATH	LIN, JONATHAN
08560038	5666309	150	11/17/1995	MEMORY CELL FOR A PROGRAMMABLE LOGIC DEVICE (PLD) AVOIDING PUMPING PROGRAMMING VOLTAGE ABOVE AN NMOS THRESHOLD	LIN, JONATHAN
08625403	5646901	150	03/26/1996	CMOS MEMORY CELL WITH TUNNELING DURING PROGRAM AND ERASE THROUGH THE NMOS AND PMOS TRANSISTORS AND A PASS GATE SEPARATING THE NMOS AND PMOS TRANSISTORS	LIN, JONATHAN
08690768	5801551	150	08/01/1996	DEPLETION MODE PASS GATES WITH CONTROLLING DECODER AND NEGATIVE POWER SUPPLY FOR A PROGRAMMABLE LOGIC DEVICE	LIN, JONATHAN
08692688	5789269	150	08/06/1996	FIELD IMPLANT FOR SEMICONDUCTOR DEVICE	LIN, JONATHAN
08978754	Not Issued	161	11/26/1997	IMPROVING FIELD LEAKAGE BY USING A THIN LAYER OF NITRIDE DEPOSITED BY CHEMICAL VAPOR DEPOSITION	LIN, JONATHAN
08980883	5908308	150	11/26/1997	USE OF BOROPHOSPHOROUS TETRAETHYL ORTHOSICLICATE (BPTEOUS) TO IMPROVE ISOLATION IN A TRANSISTOR ARRAY	LIN, JONATHAN
09241265	6211022	150	02/01/1999	FIELD LEAKAGE BY USING A THIN LAYER OF NITRIDE DEPOSITED BY CHEMICAL VAPOR DEPOSITION	LIN, JONATHAN
09563956	Not Issued	161	05/02/2000	Vehicle internal rear-view mirror with electronic information display means	LIN, JONATHAN
10083132	6965945	150	02/27/2002	SYSTEM AND METHOD FOR SLOT BASED ARL TABLE LEARNING AND CONCURRENT TABLE	LIN, JONATHAN

				SEARCH USING RANGE ADDRESS INSERTION BLOCKING	
10083291	6920552	150	02/27/2002	NETWORK INTERFACE WITH DOUBLE DATA RATE AND DELAY LOCKED LOOP	LIN, JONATHAN
10083591	6813620	150	02/27/2002	BINARY SEARCH ENGINE AND METHOD	LIN, JONATHAN
10083594	6981058	150	02/27/2002	SYSTEM AND METHOD FOR SLOT BASED ARL TABLE LEARNING WITH CONCURRENT TABLE SEARCH USING WRITE SNOOP	LIN, JONATHAN
10090797	Not Issued	30	03/06/2002	Pointer based binary search engine and method for use in network devices	LIN, JONATHAN
10098337	6934866	150	03/18/2002	NETWORK INTERFACE USING PROGRAMMABLE DELAY AND FREQUENCY DOUBLER	LIN, JONATHAN
10377709	Not Issued	41	03/04/2003	Planning system of cargo supply and transportation	LIN, JONATHAN
10905223	Not Issued	20	12/22/2004	COMPUTER SYSTEM AND RELATED METHOD FOR GENERATING PROGRAM CODES DESCRIBING RELATIONSHIPS OF NUMEROUS FUNCTION NAMES AND NUMEROUS CONTROL CODES OF A DEVICE	LIN, JONATHAN
10965728	Not Issued	95	10/18/2004	BINARY SEARCH ENGINE AND METHOD	LIN, JONATHAN
11100427	Not Issued	20	04/07/2005	Notification method and device for service providers	LIN, JONATHAN
11149182	Not Issued	30	06/10/2005	Network interface with double data rate and delay locked loop	LIN, JONATHAN
11164777	Not Issued	19	01/01/0001	SYSTEM AND METHOD FOR MODIFYING FIRMWARE OF AN OPTICAL STORAGE MEDIUM DEVICE WITHOUT REQUIRING A COMPILING PROCESS	LIN, JONATHAN
11180628	Not Issued	94	07/14/2005	NETWORK INTERFACE USING PROGRAMMABLE DELAY AND FREQUENCY DOUBLER	LIN, JONATHAN

60273514	Not Issued	159	03/07/2001	Slot based ARL table learning with concurrent table search using search range address insertion blocking	LIN, JONATHAN
60273515	Not Issued	159	03/07/2001	Slot based ARL table learning with concurrent table search using insertion write snoop	LIN, JONATHAN
60273516	Not Issued	159	03/07/2001	Pointer based binary table search	LIN, JONATHAN
60273517	Not Issued	159	03/07/2001	Binary search in four parallel banks	LIN, JONATHAN
60276034	Not Issued	159	03/16/2001	2.5G bit ethernet interface design with programmable delay element and frequency doubler	LIN, JONATHAN
60276104	Not Issued	159	03/16/2001	2.5G Bit ethernet interface design using DLL to enhance performance and yield	LIN, JONATHAN
07387007	4963879	150	07/31/1989	DOUBLE SKIRT OMNIDIRECTIONAL DIPOLE ANTENNA	LIN, JONATHAN X.
09572298	6829763	150	05/16/2000	PARTITIONED EXECUTIVES STRUCTURE FOR REAL-TIME PROGRAMS	LINCOLN, JONATHAN A.
09480657	Not Issued	161	01/11/2000	Method of identifying and treating inflamed tissue	LINDNER, JONATHAN
10480438	Not Issued	30	10/13/2004	Method and measuring changes in microvascular capillary blood flow	LINDNER, JONATHAN
60113199	Not Issued	159	12/22/1998	DRUG DELIVERY INCORPORATING LEUKOCYTES WITH ENDOCYTOSED MICROBUBBLES AND DRUGS	LINDNER, JONATHAN
60116793	Not Issued	159	01/21/1999	METHOD OF IDENTIFYING AND TREATING INFLAMMED TISSUE	LINDNER, JONATHAN
60735517	Not Issued	20	11/11/2005	Deposit contrast agents and methods	LINDNER, JONATHAN
60712657	Not Issued	20	08/30/2005	Method and system for depot contrast agent for perfusion imaging with intravenous administration	LINDNER, JONATHAN R.
09923769	Not Issued	41	08/06/2001	Hybrid communications and interest management system and	LINDO, JONATHAN

				method	
09923771	Not Issued	41	08/06/2001	Network communications protocol	LINDO, JONATHAN
11169522	Not Issued	20	06/28/2005	Recording and replaying computer programs	LINDO, JONATHAN
60231289	Not Issued	159	09/08/2000	Network communications protocol	LINDO, JONATHAN
60231390	Not Issued	159	09/08/2000	Hybird communications and interest management technique	LINDO, JONATHAN
09659215	Not Issued	161	09/11/2000	ONLINE LIVE SEARCH SYSTEMS	LINDO, JONATHAN B.

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
07995587	5363501	150	12/22/1992	A METHOD FOR COMPUTER SYSTEM DEVELOPMENT, VERIFICATION AND TESTING USING PORTABLE DIAGNOSTIC/TESTING PROGRAMS	PULLELA, SOMAYAJULU S. K.
10090797	Not Issued	30	03/06/2002	Pointer based binary search engine and method for use in network devices	PULLELA, SOMAYAJULU S.K.
11192853	Not Issued	20	07/29/2005	Hybrid pixel pattern for video buffer in DDR	PULLELA, SOMAYAJULU SATYA

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